

### **Amendments to the Claims**

Please amend the claims in the manner indicated.

1. (currently amended) An apparatus, comprising:  
write cache storage to store cache lines of write data;  
a flush dispatcher coupled to the write cache storage to dispatch the cache lines to  
memory;  
control logic to:  
select a first cache line with a first address in the write cache storage for  
dispatching;  
determine if a second cache line in the write cache storage has a second  
address within a ~~predetermined~~ range of the first address of less  
than one page; and  
dispatch the first and second cache lines if the second address is within the  
~~predetermined~~ range of the first address.
2. (cancelled)
3. (original) The apparatus of claim 1, wherein control logic to select a first cache  
line includes logic to select an oldest cache line in the write cache storage.

4. (currently amended) A method, comprising:
- receiving a plurality of write requests and storing the plurality of write requests in a plurality of cache lines in a write cache storage;
- selecting a first one of the plurality of cache lines for dispatching to a memory, wherein the first one of the plurality of cache lines has a first address;
- determining if a second one of the plurality of cache lines has a second address within a ~~predetermined~~ programmable range of the first address;
- dispatching the first and second ones of the plurality of cache lines to the memory if the second address is within the ~~predetermined~~ programmable range of the first address; and
- dispatching the first one but not the second one of the plurality of cache lines to the memory if the second address is not within the ~~predetermined~~ programmable range of the first address;
- wherein the programmable range is less than one page.
5. (currently amended) The method of claim 4, wherein said selecting a first one includes selecting an oldest of the plurality of cache lines.
6. (currently amended) The method of claim 4, further comprising programming the ~~predetermined~~ programmable range before selecting.

7. (currently amended) A machine-readable medium having stored thereon instructions, which when executed by a processor cause said processor to perform:
- receiving a plurality of write requests and storing the plurality of write requests in a plurality of cache lines in a write cache storage;
  - selecting a first one of the plurality of cache lines for dispatching to a memory, wherein the first one of the plurality of cache lines has a first address;
  - determining if a second one of the plurality of cache lines has a second address within a ~~predetermined~~ programmable range of the first address;
  - dispatching the first and second ones of the plurality of cache lines to the memory if the first address is within the ~~predetermined~~ programmable range of the second address; and
  - dispatching the first one but not the second one of the plurality of cache lines to the memory if the second address is not within the ~~predetermined~~ programmable range of the first address;
  - wherein the programmable range is less than one page.
8. (currently amended) The medium of claim 7, wherein said selecting a first one includes selecting an oldest of the plurality of cache lines.
9. (currently amended) The medium of claim 7, further comprising programming the ~~predetermined~~ programmable range before selecting.
- 10-30. (cancelled)